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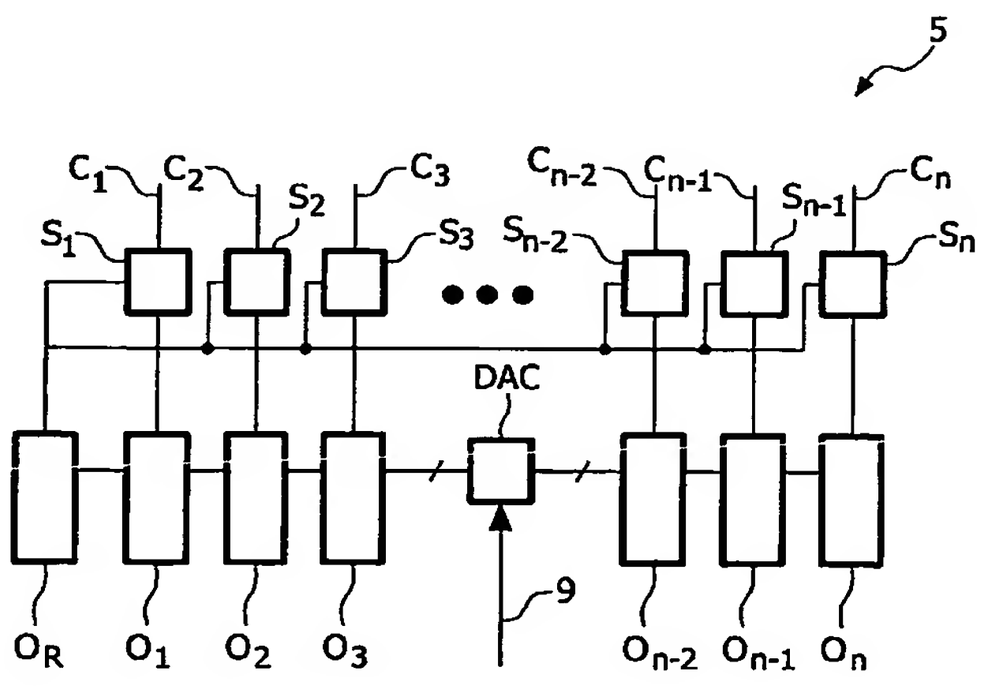
(54) Title: CIRCUIT ARRANGEMENT FOR DRIVING A DISPLAY ARRANGEMENT



(57) Abstract: The invention concerns a circuit arrangement (2) for driving a display arrangement (1). Further it concerns a display arrangement (1) and a method for driving a display arrangement (1). To provide an arrangement (2) having a good offset cancellation combined with high quality illustrating of an image the circuit arrangement includes column driving means (5) for driving n column electrodes (C) and row driving means (4) for driving m row electrodes (R) of the display arrangement (1), wherein the column driving means (5) comprises n output channels (O), each output channel (O) having a column electrode (C) assigned and is arranged for providing a respective column voltage to the assigned column electrode (C), an additional output channel (O_R) is arranged for providing a respective column voltage, whereas each of the n column electrodes (C) is connectable to the additional output channel (O_R). The additional output channel (O_R) will be calibrated at first. Then the additional output channel will successively replace the output channel (O_1- O_n), whereas during replacement the output channels (O_1- O_n) will be calibrated sequentially. So the time required for offset cancellation will not reduce the time for settling the column voltage.

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